AUVIDEA

JNX40S

TECHNICAL REFERENCE MANUAL

SCOPE OF WORK
Providing technical information and documentation to the product line JNX40S for NVIDIA Jetson Orin Nano & Orin NX

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SECTION 1 Document revisions and changes

Document version	Changes
V1.0	Initial document, internal verification process
V1.1	Internal verification process
V1.15	Updated product revisions and changes
V1.16	Updated product revisions and changes (JNX42-LM)
V1.20	Updated product revisions and Changes (JNX40S)

SECTION 2 Product revisions and changes

Product version	Changes
38556	▪ First version
38556-2	▪ swapped CAM1_PWR and CAM2_PWR (rev 1 needs device tree change, to correct this in software) – rev 2 is now compatible to the NVIDIA device tree ▪ RXD pulldown added ▪ added coin cell holder for 6.8mm cells (MC621, V364, SC621, MS518SE, MS621FE) – optional ▪ added EEPROM ▪ changed IMU to BMI088 ▪ added Harting 3 pin power input connector (Harting 14110313002000): 1 - 24V AO 2 - 24V (GPIO) 3 – GND connect ignition controlled power to pin 2 (J37) ▪ added J30 coin cell holder for 10mm cells (CR1025) – optional ▪ added J18 coin cell holder for 20mm cells (CR2032) – default configuration ▪ added JST-SM alternative to RJ45 - optional
38556-3	▪ change UART routing to connector (add console UART) ▪ added 2nd USB 2.0 hub (micro USB J12) to integrate service port deluxe features (USB0 to Jetson and USB to MCU) – requires a special firmware ▪ converted MCU to USB connection so that MCU can do USB to UART conversion (Jetson console and MCU UART) ▪ added signals to M.2 Wifi (J24) – for GPS RTK M.2 modules - timepulse: Jetson input (pin 195 inverted) - WheelTick: Jetson output (pin 192 inverted) – W230R only (with ZED-F9R) - Dir: Jetson output (pin 199 inverted) – W230R only (with ZED-F9R)
38556-4	▪ added MCU reset to service port USB hub ▪ added Jetson reset (pin 206) to standard USB 2.0 hub ▪ changed J19 from CAN to user UART (3.3V) ▪ added USB bypass to service port USB hub ▪ M.2 Key B: connect 4 additional GPIOs (dir, wheeltick, geofence, timepulse) ▪ changed DF12-20 pin 5 to carrier board power on (inverted with MOSFET for isolation, 0: enable 48V pushup power supply – for 38624 add on module) ▪ added J38 (bottom side) with SPI0 1.8V and 2 CS
38556-4.1	▪ J38 1.2mm to the outside - conflict with SIMMCOM module - only move 1.2mm for PickPlace to the outside ▪ added eSIM option ▪ upgraded USB 3.0 to 10Gbps support ▪ added 1 wire 128 byte EEPROM bus to J28 pin 6 and the MCU PB1 and added this EEPROM on JNX42 (max 8 EEPROMs on this bus) ▪ converted LED connector to new pinout (GND, LED, 5V, 12V) ▪ added LTE UART (connected to MCU PB6 and PB7) ▪ added INA139 to measure total current consumption on 12V rail (MCU PB0) ▪ added test points: GND, 3.3V_LDO, IO, CLK, J28-P6, LED (MCU), Vin, 12V
38556-4.1.1	▪ max15V input (TI 5V supply) – changed the 12-19V text on the bottom side

	▪ JLC version ▪ MCU: set Boot0 fuse in MCU when flashing
38556-4.2	**Additional Molex connector options:** ▪ moved vertical Molex Microfit 2 pin ▪ added horizontal Molex Microfit 2 pin **CR2032 enhancement:** ▪ added D68 to disable charging of the CR2032 Lithium cell ▪ J41: added 2 pin RTC battery connector (RPI compatible) **GbE new feature:** ▪ J30: added JST-GH 10 pin connector for GbE (on the bottom side) – moved RTC holder **Power supply enhancement: now 12V power in (12V absolute max)** ▪ changed 5V and 3.3V power supplies to 12A type ▪ removed 5.5V LDO => support input voltages up to 24V ▪ removed 5V peripheral supply and replaced it by high-side switch **USB2.0 connector: additional option** ▪ populate J8, when USB 2.0 hub is not populated (LC version) **Basic overvoltage protection** ▪ added 24V TVS diode for basic overvoltage and reverse voltage protection (reverse voltage is shorted – this will potentially damage the diode – please keep the duration of the reverse voltage applied very short) – diode on the top side (so the LC version has it also) **LC: Harting on the top side** ▪ added 3 pin Harting connector on the top side (for the LC version without dual USB connector) ▪ moved all components to the top side for the LC version – the LC version is now top side only (with a flat bottom side for minimal height)
38556-5	▪ convert U6 to 12A type for V5.0_CM ▪ change J28 (DF12-20) pin out for PoE++ (600mA) and changed gender type - to avoid that incompable modules are plugged ▪ replaced TI 5V buck with MOSFET high side switch (to support voltages up to 24V) => 12V -19V spec'ed ▪ **changed name to JNX42P** (P for high power PoE++)
38556-5.1	▪ fix PoE++ magnetics pinout ▪ add R44 and connected MCU reset to pin9 J28 ▪ no stencil mask change to rev 5 (except R44)
38556-6	▪ fix PoE++ magnetics pinout (DF12-20) ▪ J22: converted 1.8V SPI bus to bi-directional GPIOs (I/O voltage configurable from 1.8V to 5.0V via VIO) ▪ J22: added PWM IO, voltage level set by VIO, pin 218) ▪ J22: now 12 pin JST-GH, added 5.0V and VIO pin (1.8V removed) ▪ J32: 1025 battery holder removed ▪ added diode for J18 battery holder (to disable any cell charging) ▪ J27, J29: vertical JST-GH ▪ J26: vertical JST-GH with 3.3V bi-directional level shifter (I2S or optional GPIOs) - PWR: 3.3V default, 1.8V optional ▪ J25: vertical JST-GH UART0, level shifted to 1.8V or 3.3V (default) with bi-directional level shifters (optional GPIOs). PWR: 5V only ▪ CSI-2: support level conversion to 1.8V with I2C mux (optional - to support Framos cameras) ▪ CSI-2: removed TRS diodes

	▪ J7: power out: pin3 is 12V out (default) or injection start input (optional) ▪ J17 p4 (M2E-1PPS): M.2 key E (J24 p44) 1PPS inverted, level shifted to 1.8V and connected to I2S SDIN (p195). This signal is connected via bi-dir level shifter (3.3V) to J17 p4. This pin can be a Jetson input (p195), if J24 p44 (M.2 key E) is floating or low. Auvidea M.2 cards (GPS and 5G Ethernet) supply 1PPS on pin 44. This can be used as a very precise time source. ▪ added J30: GbE on 10p JST-GH (bottom side vertical) ▪ added 18V SMA TVS diode for minimum power input protection (connected to return path of power module) ▪ change power supply to LM823 module: 1. connect to VUSB 5V (so that it can be power cycled) 2. connect to 3.3V with high side switch (controlled by USB enable - pin 220) - similar to JN30D rev 4 ▪ moved Molex MicroFit 2 pin power connector closer to the edge and added a 2nd (horizontal 2 pin type) ▪ changed LM823 (2.4G) footprint to LM843 (5G) footprint with 2 additional ground pads
38545-3	▪ changed to 4 CSI-2 connectors with 2 lanes each ▪ moved CSI-2 connectors to the bottom side ▪ moved I2C3, CAN and SPI0 to the top ▪ **changed name to JNX40P**
38545-4	▪ implemented changes for new "super" series of the Orin ▪ added 9V power supply (for super modes = 40W) ▪ added 2 pin RTC connector (J5) - RPi compatible ▪ added compute module power rail (5V/9V) to J17 pin 6 ▪ **changed name to JNX40S**

SECTION 3 Overview

3.1 JNX40S

The JNX40S and the JNX41S models are designed to support the super mode of the Orin Nano/NX. This enhanced mode is available for all Orin Nano/NX modules with the implementation of the JetPack 6.2.

The JNX40S supports low level remote system management features with optional MCU firmware installed:

- remote power cycle
- change of boot device
  Orin: primary: SSD, secondary: USB
- Limited remote debugging

Please note that the JNX44/45/46 offer enhanced system management features.
The features above are implemented with the following hardware:

- UART connection between MCU and LTE module
  this requires a LTE module with USB and UART (e.g. SIMCOM 7600)
  it also requires firmware enhancement of the MCU
- Console UART connection (Jetson and MCU)
- Easy integration into passively cooled systems
- Easy flashing: just connect a USB OTG cable (Auto Flash)
- High performance storage: M.2 NVME PCIe x4

3.2 JNX40S features and comparison

Description	JNX40S
Compatible Modules	Orin Nano/NX
Super mode support	✓
Power in	19V
HDMI 2.0	up to 4kp60
Fan connector	5V for original NVIDIA heatsink/fan
M.2 key B	USB 2.0 for LTE
M.2 key E	PCIe x1 + USB 2.0 (Wifi)
M.2 NVME key M	✓ 2280 SSD
USB 3.0	3x 3.3
micro-USB	OTG (for flashing) and host mode
Auto Flashing	✓ (plug in host cable and flash)
USB 2.0	1x USB 2.0 (JST-GH, J8, J23)
Ethernet	5GbE or 10GbE (with M.2 Key E) (J24)
PoE option	PoE++ (802.3bt Type 4 72W) (with the use of our Px modules
CSI	4x CSI-2 (2 lanes) plus camera LED (hardware sync available)
CAN RX / TX	✓

UART	2x (1x when using LTE module) (3.3V)
I2C	3x (3.3V)
I2S	1x (3.3V)
SPI	1x (1.8V)

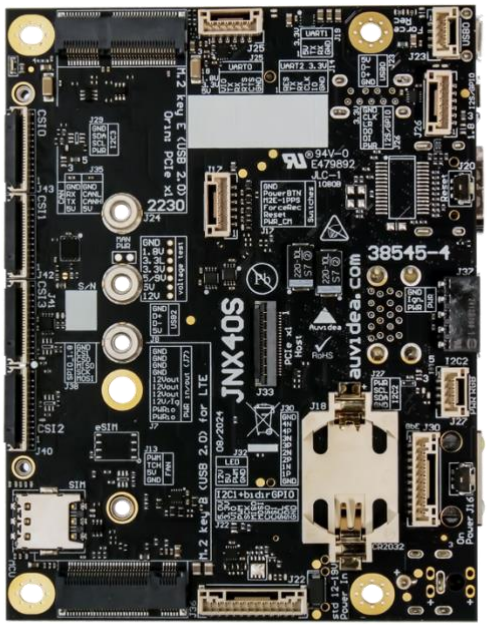
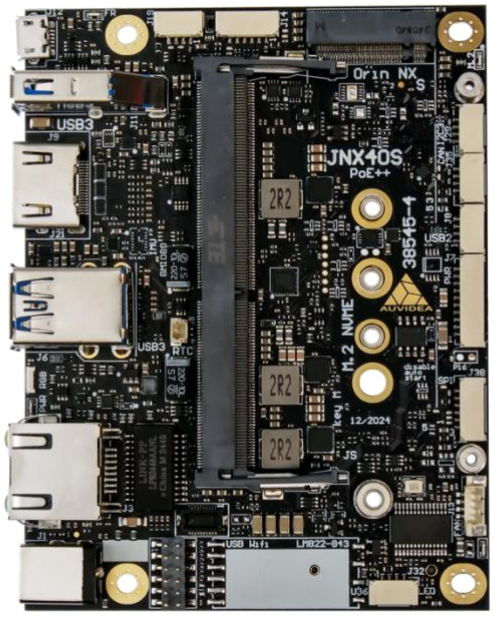
Host mode: peripheral devices like mouse and keyboard may be connected
Device mode: the USB port is in “slave” mode to support connection to host PC (e.g. flashing or remote access – virtual network connection - 192.168.55.1)

UART0: user UART port with RTS and CTS (1.8V level)
UART2: console UART port (only available with rev 3)

3.3 Technical specifications

Description	
HDMI	standard HDMI connector (2.0)
USB 3.3	10Gb/s
Physical size	80x 104.6mm
Mounting holes	4x 3.2mm (for M3)
Temperature range	0 to 70°C (extended range optional)
Humidity	noncondensing
Longevity	no temperature sensitive components (like electrolytic capacitors)

3.4 Model pictures

	Top side	Bottom side
JNX40S		

3.5 Power consumption

Description	JNX40S
Carrier board logic	< 1 watt
3.3/5V power converter efficiency	> 90%
Power in converter efficiency	> 90%

3.6 Differentiating features

- flexible design and manufacturing
- designed and manufactured in Germany
- in-house fully automated production line with 3D AOI
- special configuration possible with minimum purchase of 25 pcs (display port, M.2, PoE, 48V power in, super cap option, 2 RPi camera module connectors, SPI, I2C, switches and more.)
- design services: you architect your custom carrier board and Auvidea designs and produces it - please ask for a quote

SECTION 4 FEATURES

4.1 Crypto chip (optional)

The crypto chip is optional and can be used to protect your software investment. Please check the data sheet of the manufacturer for details.
Model: ATSHA204A-MAHDA-T
https://ww1.microchip.com/downloads/en/DeviceDoc/ATSHA204A-Data-Sheet-40002025A.pdf

4.2 MCU chip

A on board MCU for watchdog and low level system management functions (via LTE). The MCU can receive text messages via an UART connection to compatible LTE M.2 cards (e.g. Simcom SIM7600). With these messages low level system management can be performed. Please note that this requires an optional MCU firmware. The source code to this firmware may be licensed.

- power cycle or reset
- change of boot order

4.3 USB 2.0 hub

This board features a 4 port USB 2.0 hub controller (GL850G).
Uplink: USB1 (115/117) of the Jetson
Port 1: USB 2.0 of dual USB 3 connector (lower port)
Port 2: USB 2.0 of dual USB 3 connector (upper port)
Port 3: M.2 key B LTE slot
Port 4: LM module (LM) or M.2 key E slot (M2) plus J8 (shared)

Compatibility issue:
Potential issues with the Xavier NX compute module. USB 2.0 hub will not be initialized. Please do a warm reboot to fix that issue.
Issue is fixed with Revision 3.1.

SECTION 5 Pinout description top side

Please note that the software GPIO number differs from the socket pin number. This software GPIO number must be computed with a special formula and differs between the various compute modules. Please see appendix B for details.

5.1 J1 - Power input jack

5.5/2.5mm barrel jack

Pin	Description	Note
1	12V - 19V	12V nominal (absolute max. 24V)
2	GND	

If no power module is plugged in, the supply voltage must be bridged by inserting 5 or 6 2mm jumpers. If no jumpers are plugged then power is not applied.
Optionally the J34 Molex power connector (436500216) may be populated. Optionally power may be supplied via J7 (2 pins)

5.2 J3 - Ethernet (GbE)

Standard RJ45 pinout with PoE capable magnetics class 3 and 4 (PD or PSE). Connected to the Ethernet controller on the compute module. PoE power is routed to the optional power module.

5.3 J4 - M.2 PCIEx4 NVME SSDs

Please note that only NVME SSDs are supported. SATA SSDs are not supported. We recommend the 128GB Transcend SSDs (TS128GMTE110S). This SSD is standard in Auvidea embedded systems.

5.4 J34 - Molex power connector (optional – J1 must not be populated)

To support an extended power input range (12V – 48V) please order one of our many available power modules, for more details please see 5.16.

5.5 J5 – RTC

Carrier board RTC functions are not enabled. Jetson RTC functions are supported.

5.6 J6 - USB 3.3

Power cycling of the USB ports possible.

USB 3.0

Pin	Description	Socket pin	Note
1	USB power		5V 1.0A (power switch enable: pin 220 – 0: enable)
2	USB2-D_N		USB 2.0 hub chip port 1
3	USB2-D_P		
4	GND		

5	USB3_RX2_N	51	
6	USB3_RX2_P	53	
7	GND		
8	USB3_TX2_N	57	
9	USB3_TX2_P	59	
10	USB power		5V 1.0A (power switch enable: pin 224 – 0: enable)
11	USB22-D_N		USB 2.0 hub chip port 2
12	USB22-D_P		
13	GND		
14	USB3_RX1_N	39	
15	USB3_RX1_P	41	
16	GND		
17	USB3_TX1_N	45	
18	USB3_TX1_P	47	
19	GND		

5.7 J7 - PWR out

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	PWR_IN		connected to the power input jack (J1) and (J34)
2	PWR_IN		connected to the power input jack (J1) and (J34)
3	12V out		provided by power in or output of power module
4	12V out		provided by power in or output of power module
5	12V out		provided by power in or output of power module
6	12V out		provided by power in or output of power module
7	GND		
8	GND		
9	GND		
10	GND		

5.8 J8 - USB 2.0

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	USB power		5V 1A max (no current limiter)
2	USB2_D_N	121	
3	USB2_D_P	123	
4	GND		

Internal USB 2.0 JST-GH connector to connect to internal USB 2.0 add-on modules (like U100, U110, U120 and more).

USB Hub Chip – GL850G

5.9 J9 - HDMI

Standard pinout
CEC is not supported (pin 13 of HDMI connector)

Power pin 18 is current limited by PTC fuse (5V 50mA)

5.10 J11 - USB 3.1

USB 3.1 Type A standard pinout
In Rev 1: power control is always on (5V 1A)
In Rev 2 and up: Controllable with GPIO_03 (socket pin 126) power on[0]/off[1], default on[0], 5V 1A
See Appendix B for documentation on how to configure and use GPIOs.

Power cycling of the USB ports possible.

USB 3.1

Pin	Description	Socket pin	Note
1	USB power		5V 1.5A (power switch enable: pin 126 – 0: enable)
2	USB2-D1_N	121	
3	USB2-D1_P	123	
4	GND		
5	USB3_RX_N	161	
6	USB3_RX_P	163	
7	GND		
8	USB3_TX_N	166	
9	USB3_TX_P	168	
10	GND		

5.11 J12 - Micro USB

Standard pinout
OTG support (to flash the compute module)
See Appendix B for documentation on how to configure and use GPIOs.

5.12 J13 - Fan connector

Pico blade 1.5mm

Pin	Description	Socket pin	Note
1	GND		
2	5V		1A max
3	TACH	-	Not connected
4	PWM	230	

5.13 J14 – UART1 Connector

The white silkscreen reads “UART2 3.3V”, it should read “UART1”

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	MCU_NRST	-/-	MCU reset
2	UART1_TXD	203	UART connection between M.2 LTE card and MCU - do not drive this line - this is just for debugging purposes

3	UART1_RXD	205	UART connection between M.2 LTE card and MCU - do not drive this line - this is just for debugging purposes
4	SWCLK		programming of MCU
5	SWDIO		programming of MCU
6	GND		-/-

The UART connection of the MCU to the LTE modem is used for a feature to let the MCU receive text messages via LTE for low level system control (like power cycle) or system management.

In addition, the UART0 (debug UART port) is connected to the MCU. The MCU can be a watchdog to monitor the Jetson via UART0. With a second UART the MCU is connected to the LTE module and can communicate to the user via LTE.

5.14 J15

J15: power module header (10 or 12 pin 2mm pin header - male)

Pins	Description	Note
1,3,5	DC input (2A max per pin)	
7,9,11	GND input (2A max per pin)	
2,4,6	DC supply to carrier board (2A max per pin)	
8,10,12	GND to carrier board (2A max per pin)	

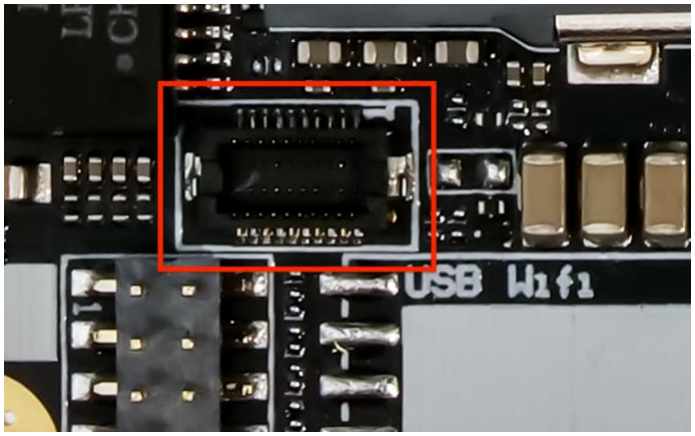
5.15 J19 – Debug Console UART2

The description on the PCB “UART1” is wrong, it should read “UART2”

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	5V	-/-	5V 1A max (no current limiter)
2	UART2_TXD	236	
3	UART2_RXD	238	
4	GND		

5.16 J28: PoE++ extension connector



Standard options:

- P12 isolated PoE input module (25W PoE PD, 802.3at, class 4) (38568-x)
- P17P 4PPoE on request (40-70W PoE PD, 802.3bt, class 5-8)
- P14 PoE PSE 24V power module (38624)
- P14P PoE PSE-48V is available for projects.

Please note that the standard connector is only compatible to our P-variant power modules. (P14P, P17P, etc.)

J28: PoE++ extension connector

Pin	Description	Socket pin	Note
1	VDD_RTC	235	RTC power supply for Jetson
2	I2C1_SDA	191	3.3V level
3	PFO_INT	212	1.8V level
4	I2C1_SDA	189	3.3V level
5	GPIO_04	127	1.8V level
6	nc		reserved for future use
7	1.8 V		
8	nc		reserved for future use
9	3.3 V		
10	5 V		
11	PFO		Power fail (to MCU pin PA0) – 3.3V level
12	FAN_TACH	208	
13	3.3 V_LDO		3.3V (from LDO, 50mA max)
14	FAN_PWM	230	
15	GND		
16	GND		
17	M1		Ethernet center pin of magnetics (for PoE)
18	M3		Ethernet center pin of magnetics (for PoE)
19	M2		Ethernet center pin of magnetics (for PoE)
20	M4		Ethernet center pin of magnetics (for PoE)

Optional I/O module:
The J15 and J28 can be used to create a wide variety of I/O and power modules:

- GPIO extender
- ADC or DAC converter (for analog inputs or outputs)
- PWM controller
- LED controller
- fan controller
- motion (IMU), temperature and other sensors
- RTC supply
- Special power supplies (including UPS)
- and many more

Auvidea offers a custom design service. Please contact us for details.

5.17 J29 - I2C

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	5V		5V 1A max (no current limiter)

2	CAM_I2C_SCL	213	3.3V level
3	CAM_I2C_SDA	215	3.3V level
4	GND		

5.18 J32 - CAM LED

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	12V		12V 1A (no current limiter)
2	12V		12V 1A (no current limiter)
3	CAM_LED	218	Inverted GPIO, wired OR CSI-MCLKs OUT: open drain (3.3V to 12V)
4	CAM_LED	218	Inverted GPIO, wired OR CSI-MCLKs OUT: open drain (3.3V to 12V)

CAM_LED: open drain output to drive cathode of camera LED.
Controlled by 3 signals which are OR ‘red together:
J5 pin 5 (CSI CD camera)
J10 pin 5 (CSI AB camera)
Compute module GPIO_12: (socket pin 218) 0: LED off, 1: LED on, float: LED on
CAM_LED flash signal can be used to control external camera LED. When using external LED please limit current with external resistor. Connect cathode to CAM_LED pin and anode to 3.3V to 12V power. Maximum current 2A.

5.19 J35 - CAN

JST-GH 1.25mm (LM and M2)

Pin	Description	Socket pin	Note
1	PWR		5V 1A (no current limiter)
2	CAN_H	CAN	Integrated CAN transceiver (TJA1051)
3	CAN_L	CAN	Integrated CAN transceiver (TJA1051)
4	GND		

CAN_RX/ CAN_TX output option:
CAN_RX and CAN_TX: must not be directly connected to the CAN bus as there is the risk to damage the Jetson compute module.
For CAN_RX and CAN_TX, an external CAN transceiver module must be used. Our carrier boards are populated with the transceiver: TJA1051 (58V).
If you need an isolated CAN you can use the 38477 add-on module.
To enable the output of the native RX/TX CAN signals, 4 resistors have to be moved. This is a hardware strapping option.

5.20 5.21 J38 SPI

Pin	Description	Socket pin	Note
1	SPI0_MOSI	89	
2	SPI0_SCK	91	
3	SPI0_MISO	93	
4	SPI0_CS0	95	
5	SPI0_CS1	97	

6	GND

5.21 PoE and Power module connector

These 2 connectors allow to fit the JNX42 with an optional power module, to extend the power input capabilities of the JNX42.

5.22 U10 – IMU BMI088

The BMI088 is a high-performance 6-axis inertial sensor that allows for highly accurate measurement of orientation and detection of motion along three orthogonal axes. Consisting of a 16-bit digital, triaxial accelerometer and a 16-bit digital, triaxial gyroscope, the BMI088 is unique in the class of high-performance IMUs used in harsh environments such as those ones in drones and robotics applications.
I2C bus: I2C1 (183/185)
Accelerator: 0x18
Gyro: 0x68
Link: https://www.bosch-sensortec.com/products/motion-sensors/imus/bmi088/

5.23 LED - PWR

GPIO socket pin 178* on[1]/off[0], default: off[0]. Pinout description bottom side. This GPIO pin is supported by the NANO. Other SOMs may not be able to control this GPIO. Rev 3 will move this to a different GPIO.

*Revision 4.1: GPIO socket pin 95. on[1]/off[0], default: off[0].

5.24 LED - Eth

Green LED (Link): The green LED on the LAN port indicates that there is a connection.
Yellow LED (Activity): The yellow indicates that there is activity here, so there is data traffic

5.25 Force Recovery button

SECTION 6 Pinout description bottom side

6.1 J20 - Reset Button

Press to reset the compute module.

6.2 J22 - SPI

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	5.0V		5.0V 1A max (no current limiter)
2	3.3V		3.3V (current depending on version: 100 or 500mA)
3	VIO		3.3V: power to Bidirectional Level Shifter. can be switched to power input if component L22 must be removed)
4	GPIO12	218	3.3V PWM
5	SPI1_SCK	106	3.3V
6	SPI1_MISO	108	3.3V
7	SPI1_MOSI	104	3.3V
8	SPI1_CS0	110	3.3V (may be reconfigured as GPIO)
9	SPI1_CS1	112	3.3V (may be reconfigured as GPIO)
10	GEN1_I2CO_SCL	185	3.3V (pull up on Jetson)
11	GEN1_I2C0_SDA	187	3.3V (pull up on Jetson)
12	GND		

6.3 J25 UART0

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	3.3V	-/-	Can be switched to 5V or 1.8V with replacement of component L16 with eighter L24 or L20
2	UART0_TXD	99	3.3V
3	UART0_RXD	101	3.3V
4	UART0_RTS	103	3.3V
5	UART0_CTS	105	3.3V
6	GND		

6.4 J27 - I2C

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	5V		Can be switched to 3.3V if component R135 is replaced by R136
2	I2C1_SCL	189	3.3V
3	I2C1_SDA	191	3.3V
4	GND		

6.5 J33 PCIe X1

Pin	Description	Socket pin	Note
1	GND		
2S	GPIO_05	128	
3S	PCIE_Wake_L	179	
4	GND		
5S	PEX_L1_CRQ	182	
6S	PEX_L1_RST	183	
7	GND		
8S	PEX_RX0_N	167	
9S	PEX_RX0_P	169	
10	GND		
11S	PEX_TX0_N	172	
12S	PEX_TX0_P	174	
13	GND		
14S	PEX_CLK2_N	173	
15S	PEX_CLK2_P	175	
16	GND		
17S	3.3 V power		
18S	3.3 V power		
19	GND		
20S	5 V power		
21S	5 V power		
22	GND		

6.6 J37 - Harting power connector

Harting: 3 pin power input connector (Harting 14110313002000):

Pin	Description	Note
1	12V - 19V	input power (always on)
2	12V - 30V	ignition power (Jetson: pin 127 – inverted, MCU: PB0) – weak pullup to 3.3V – GPIO input
3	GND	

To implement ignition controlled powering the MCU firmware must be enhanced. MCU firmware support is planned. If this pin is left open /floating) the internal pullup will initiate a power up (auto start). Connect to GND to disable auto start and to manually power up the system.

- MCU monitors the pin to turn on power to the Jetson
- The Jetson monitors this pin to gracefully turn off the system

The Harting connector 14110313002000 requires the corresponding counterpart 14310313101000

6.7 J23 - USB 0

JST-GH 1.25mm

Pin	Description	Socket pin	Note
1	USB power		5V 1A max (no current limiter)
2	USB2_D_N	121	

3	USB2_D_P	123	
4	GND		

6.8 LED Force Recovery

6.9 J26 - I2S/GPIO

Implements the TBX0104RUTR 4-Bit Bidirectional Voltage-Level Translator With Automatic Direction Sensing and ±15kV ESD Protection.

Power cycling of the usb ports possible.

Pin	Description	Socket pin	Note
1	3.3V	-/-	Can be switched to 1.8V if component L9 is replaced by L10
2	I2S0_DIN	195	3.3V with bidirectional level translator
3	I2S0_DOUT	193	3.3V with bidirectional level translator
4	I2S0_FS	197	3.3V with bidirectional level translator
5	I2SO_SCLK	199	3.3V with bidirectional level translator
6	GND	-/-	

6.10 J30 - GbE

Pin	Description	Socket pin	Note
1	GND		
2	4N		
3	4P		
4	3N		
5	3P		
6	2N		
7	2P		
8	1N		
9	1P		
10	GND		

6.11 J17 – Switches

Pin	Description	Socket pin	Note
1	PWR_CM	-/-	Power supply for the compute module (5V or 9V depending on module, 9V while using the super mode)
2	Sys Reset	239	Jetson Reset
3	FORCE_RECOVERY	214	
4	M.2 1PPS	???	M.2 Key E 1PPS signal*
5	BTN_PWR_ON	-/-	
6	GND	-/-	

*The M.2 cards provided from Auvidea support the 1PPS capability, other cards my not support this feature.

6.12 J18 - CR2032

Plus contact on the top side.

6.13 J16 - Power on button

6.14 J36 - M.2 Key B 3042 /Nano SIM card slot

This slot is supposed to be used as a LTE, E-Sim is available as an optional feature

6.15 J24 M.2 Key E 2230

This supports the 1PPS signal to pin17. Compatible to wifi cards for example the Intel IX200. A 2042 card can also be mounted, the spacer for 2230 cards can be removed on request. Only PCIe x1 can be used with the Orin module, other modules feature USB 2.0.

6.16 J40 - CSI2

The configuration used by our CSI is equivalent to the one used by the NVIDIA development kit.

22 pin 0.5mm pitch FPC connector (contacts down)

Pin	Description	Socket pin	Note
1	3.3V		Power: connected with bead to 3.3V (5V optional if L17 is removed)
2	CAM_I2C_SDA	215	CAM-I2C via multiplexer (3.3V)
3	CAM_I2C_SCL	213	CAM-I2C via multiplexer (3.3V)
4	GND		
5	CAM1_MCLK		IN: wired'OR to control CAM_LED
6	CAM1_PWDN	120	OUT: LC (1.8V), LM/M2: 3.3V
7	GND		
8	NC	NC	
9	NC	NC	
10	GND		
11	NC	NC	
12	NC	NC	
13	GND		
14	CSI2_E_CLK_P	30	
15	CSI2_E_CLK_N	28	
16	GND		
17	CSI2_E_D1_P	36	
18	CSI2_E_D1_N	34	
19	GND		
20	CSI2_E_D0_P	24	
21	CSI2_E_D0_N	22	
22	GND		

6.17 J41 – CSI3

The configuration used by our CSI is equivalent to the one used by the NVIDIA development kit.

22 pin 0.5mm pitch FPC connector (contacts down)

Pin	Description	Socket pin	Note
1	3.3V		Power: connected with bead to 3.3V (5V optional if L17 is removed)
2	I2C1_SDA	191	
3	I2C1_SCL	189	
4	GND		
5	CAM0_MCLK		
6	GPIO01	118	PWDN3
7	GND		
8	NC	NC	
9	NC	NC	
10	GND		
11	NC	NC	
12	NC	NC	
13	GND		
14	CSI3_F_CLK_P	29	
15	CSI3_F_CLK_N	27	
16	GND		
17	CSI3_F_D1_P	35	
18	CSI3_F_D1_N	33	
19	GND		
20	CSI3_F_D0_P	23	
21	CSI3_F_D0_N	21	
22	GND		

6.18 J42 - CSI1

The configuration used by our CSI is equivalent to the one used by the NVIDIA development kit.

22 pin 0.5mm pitch FPC connector (contacts down)

Pin	Description	Socket pin	Note
1	3.3V		Power: connected with bead to 3.3V (5V optional if L17 is removed)
2	I2C0_SDA	187	
3	I2C0_SCL	185	
4	GND		
5	CAM1_MCLK		
6	GPIO13	228	PWDN 4
7	GND		
8	NC	NC	
9	NC	NC	
10	GND		
11	NC	NC	
12	NC	NC	
13	GND		

14	CSI1_B_CLK_P	11	
15	CSI1_B_CLK_N	9	
16	GND		
17	CSI1_B_D1_P	17	
18	CSI1_B_D1_N	15	
19	GND		
20	CSI1_B_D0_P	5	
21	CSI1_B_D0_N	3	
22	GND		

6.19 J43 - CSI0

The configuration used by our CSI is equivalent to the one used by the NVIDIA development kit.

22 pin 0.5mm pitch FPC connector (contacts down)

Pin	Description	Socket pin	Note
1	3.3V		Power: connected with bead to 3.3V (5V optional if L17 is removed)
2	CAM_I2C_SDA	215	
3	CAM_I2C_SCL	213	
4	GND		
5	CAM0_MCLK		
6	CAM0_PWDN	114	
7	GND		
8	NC	NC	
9	NC	NC	
10	GND		
11	NC	NC	
12	NC	NC	
13	GND		
14	CSI0_A_CLK_P	12	
15	CSI0_A_CLK_N	10	
16	GND		
17	CSI0_A_D1_P	18	
18	CSI0_A_D1_N	16	
19	GND		
20	CSI0_A_D0_P	6	
21	CSI0_A_D0_N	4	
22	GND		

SECTION 7 FAQ

SECTION 8 Disclaimer

Thank you for reading this manual. If you have found any typos or errors in this document, please let us know.

This is the preliminary version of this data sheet. Please treat all specifications with caution as there may be any typos or errors.

The Auvidea Team

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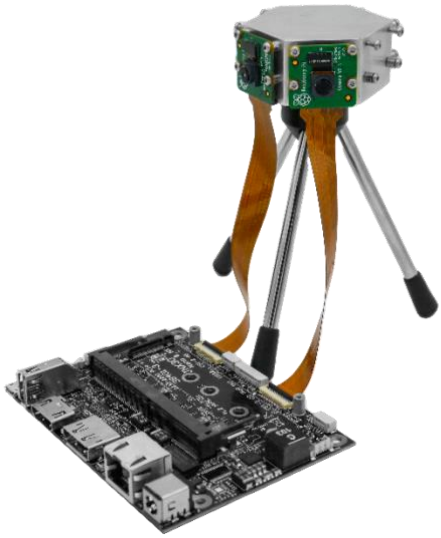
SECTION 10 Appendix A [CSI-Cameras]

10.1 Camera connection example

CSI cameras can connect to J5-CSI-2-CD and J19–CSI-2-AB connector as shown below.

	22 pin 0.5mm to … cable	Using 38237 adapter
		

SECTION 11 Appendix B [GPIO]

11.1 GPIO control

This example shows how to set and readout GPIO 414.
For different GPIO numbers replace 414 accordingly.
Export GPIO

```
nvidia@nvidia-desktop:~$ echo 414 > /sys/class/gpio/export
nvidia@nvidia-desktop:~$
```

11.1.1 Change direction to in

```
nvidia@nvidia-desktop:~$ echo in > /sys/class/gpio/gpio414/direction
nvidia@nvidia-desktop:~$
```

11.1.2 Change direction to out

```
nvidia@nvidia-desktop:~$ echo out > /sys/class/gpio/gpio414/direction
nvidia@nvidia-desktop:~$
```

11.1.3 Set GPIO low

```
nvidia@nvidia-desktop:~$ echo 0 > /sys/class/gpio/gpio414/value
nvidia@nvidia-desktop:~$
```

11.1.4 Set GPIO high

```
nvidia@nvidia-desktop:~$ echo 1 > /sys/class/gpio/gpio414/value
nvidia@nvidia-desktop:~$
```

11.1.5 Readout GPIO value

```
nvidia@nvidia-desktop:~$ cat /sys/class/gpio/gpio414/value
0
nvidia@nvidia-desktop:~$ cat /sys/class/gpio/gpio414/value
1
```

11.2 Pin to GPIO reference sheet for Xavier-NX/Nano/TX2-NX

Pin number (Socket number)	Xavier NX GPIOname	Xavier NX GPIOnumber	Xavier NX Pin direction	Nano GPIOname	Nano GPIOnumber	Nano Pin direction	TX2 NX GPIOname	TX2 NX GPIOnumber	TX2 NX Pin direction
1									
87	GPIO3_PZ.01	489	Bidirectional	GPIO3_PCC.04	228	Bidirectional	GPIO3_PL.04	412	Bidirectional
88	GPIO3_PM.00	384	Input	GPIO3_PCC.06	230	Input	GPIO3_PP.00	440	Input
89	GPIO3_PZ.05	493	Not Assigned	GPIO3_PC.00	16	Input	GPIO3_PH.02	378	Input
91	GPIO3_PZ.03	491	Not Assigned	GPIO3_PC.02	18	Input	GPIO3_PH.00	376	Input
93	GPIO3_PZ.04	492	Not Assigned	GPIO3_PC.01	17	Input	GPIO3_PH.01	377	Input
94	GPIO3_PM.04	388	Bidirectional	GPIO3_PCC.00	224	Bidirectional	GPIO3_PP.02	442	Bidirectional
95	GPIO3_PZ.06	494	Not Assigned	GPIO3_PC.03	19	Input	GPIO3_PH.03	379	Input
96	GPIO3_PM.01	385	Input	GPIO3_PCC.01	225	Input	GPIO3_PP.01	441	Input
97	GPIO3_PZ.07	495	Not Assigned	GPIO3_PC.04	20	Input	GPIO3_PY.03	515	Input
99	GPIO3_PX.04	476	Output	GPIO3_PD.01	25	Output	GPIO3_PX.00	504	Output
101	GPIO3_PX.05	477	Input	GPIO3_PD.02	26	Input	GPIO3_PX.01	505	Input
103	GPIO3_PX.06	478	Output	GPIO3_PD.03	27	Output	GPIO3_PX.02	506	Output
104	GPIO3_PY.02	482	Not Assigned	GPIO3_PB.04	12	Input	GPIO3_PV.03	491	Input
105	GPIO3_PX.07	479	Input	GPIO3_PD.04	28	Input	GPIO3_PX.03	507	Input
106	GPIO3_PY.00	480	Not Assigned	GPIO3_PB.06	14	Input	GPIO3_PV.01	489	Input
108	GPIO3_PY.01	481	Not Assigned	GPIO3_PB.05	13	Input	GPIO3_PV.02	490	Input
110	GPIO3_PY.03	483	Not Assigned	GPIO3_PB.07	15	Input	GPIO3_PV.04	492	Input
112	GPIO3_PY.04	484	Not Assigned	GPIO3_PDD.00	232	Input	GPIO3_PC.03	339	Input
114	GPIO3_PP.04	412	Output	GPIO3_PS.07	151	Output	GPIO3_PN.00	424	Output
116	GPIO3_PP.00	408	Output	GPIO3_PS.00	144	Output	GPIO3_PO.00	432	Output
118	GPIO3_PQ.05	421	Input	GPIO3_PS.05	149	Input	GPIO3_PN.01	425	Input
120	GPIO3_PP.05	413	Output	GPIO3_PT.00	152	Output	GPIO3_PN.03	427	Output
122	GPIO3_PP.01	409	Output	GPIO3_PS.01	145	Output	GPIO3_PO.01	433	Output
124	GPIO3_PQ.03	419	Input	GPIO3_PH.06	62	Input	GPIO3_PL.01	409	Input
126	GPIO3_PCC.00	264	Output	GPIO3_PI.02	66	Output	GPIO3_PL.02	410	Output
127	GPIO3_PCC.01	265	Input	GPIO3_PI.01	65	Output	GPIO3_PL.03	411	Output
128	GPIO3_PCC.02	266	Output	GPIO3_PH.07	63	Output	GPIO3_PL.00	408	Output
130	GPIO3_PCC.03	267	Output	GPIO3_PI.00	64	Output	GPIO3_PC.04	340	Output
143	GPIO3_PAA.03	251	Input				GPIO3_PZ.02	522	Output
145	GPIO3_PAA.02	250	Output				GPIO3_PZ.03	523	Input
178				GPIO3_PA.06	6	Output			
179	GPIO3_PL.02	378	Input	GPIO3_PA.02	2	Input	GPIO3_PA.02	322	Input
180				GPIO3_PA.01	1	Input	GPIO3_PA.01	321	Bidirectional
181				GPIO3_PA.00	0	Output	GPIO3_PA.00	320	Output
182	GPIO3_PK.02	370	Bidirectional				GPIO3_PA.06	326	Bidirectional
183	GPIO3_PK.03	371	Output				GPIO3_PA.05	325	Output
185	GPIO3_PCC.07	271	Bidirectional	GPIO3_PJ.01	73	Bidirectional	GPIO3_PC.05	341	Bidirectional
187	GPIO3_PDD.00	272	Bidirectional	GPIO3_PJ.00	72	Bidirectional	GPIO3_PC.06	342	Bidirectional
189				GPIO3_PJ.02	74	Bidirectional	GPIO3_PEE.00	288	Bidirectional
191				GPIO3_PJ.03	75	Bidirectional	GPIO3_PEE.01	289	Bidirectional
193	GPIO3_PT.06	446	Not Assigned	GPIO3_PJ.06	78	Input	GPIO3_PJ.01	393	Input
195	GPIO3_PT.07	447	Not Assigned	GPIO3_PJ.05	77	Input	GPIO3_PJ.02	394	Input
197	GPIO3_PU.00	448	Not Assigned	GPIO3_PJ.04	76	Input	GPIO3_PJ.03	395	Input
199	GPIO3_PT.05	445	Not Assigned	GPIO3_PJ.07	79	Input	GPIO3_PJ.00	392	Input
203	GPIO3_PR.02	426	Output	GPIO3_PG.00	48	Output	GPIO3_PW.02	498	Output
205	GPIO3_PR.03	427	Input	GPIO3_PG.01	49	Input	GPIO3_PW.03	499	Input
206	GPIO3_PR.00	424	Input	GPIO3_PV.00	168	Input	GPIO3_PU.00	480	Input
207	GPIO3_PR.04	428	Not Assigned	GPIO3_PG.02	50	Input	GPIO3_PW.04	500	Input
208	GPIO3_PQ.02	418	Input	GPIO3_PZ.02	202	Input	GPIO3_PX.04	508	Input
209	GPIO3_PR.05	429	Not Assigned	GPIO3_PG.03	51	Input	GPIO3_PW.05	501	Input
211	GPIO3_PS.04	436	Not Assigned	GPIO3_PBB.00	216	Input	GPIO3_PJ.04	396	Input
212	GPIO3_PQ.01	417	Input	GPIO3_PV.01	169	Input	GPIO3_PC.01	337	Input
213	GPIO3_PP.02	410	Bidirectional	GPIO3_PS.02	146	Bidirectional	GPIO3_PO.02	434	Bidirectional
214	GPIO3_PG.00	336	Input	GPIO3_PX.06	190	Input	GPIO3_PFF.01	529	Input
215	GPIO3_PP.03	411	Bidirectional	GPIO3_PS.03	147	Bidirectional	GPIO3_PO.03	435	Bidirectional
216	GPIO3_PQ.06	422	Input	GPIO3_PZ.00	200	Input	GPIO3_PEE.02	290	Input
218	GPIO3_PCC.04	268	Not Assigned	GPIO3_PY.02	194	Input	GPIO3_PC.02	338	Input
219	GPIO3_PO.02	402	Bidirectional	GPIO3_PP.05	125	Bidirectional	GPIO3_PG.02	370	Bidirectional
220	GPIO3_PT.02	442	Output	GPIO3_PE.02	34	Bidirectional	GPIO3_PM.03	419	Output
221	GPIO3_PO.03	403	Bidirectional	GPIO3_PP.04	124	Bidirectional	GPIO3_PG.03	371	Bidirectional
222	GPIO3_PT.03	443	Input	GPIO3_PE.01	33	Input	GPIO3_PM.00	416	Input
223	GPIO3_PO.04	404	Bidirectional	GPIO3_PP.03	123	Bidirectional	GPIO3_PG.04	372	Bidirectional
224	GPIO3_PT.04	444	Bidirectional	GPIO3_PE.00	32	Bidirectional	GPIO3_PM.01	417	Bidirectional
225	GPIO3_PO.05	405	Bidirectional	GPIO3_PP.02	122	Bidirectional	GPIO3_PG.05	373	Bidirectional
226	GPIO3_PT.01	441	Bidirectional	GPIO3_PE.03	35	Bidirectional	GPIO3_PM.02	418	Bidirectional
227	GPIO3_PO.01	401	Bidirectional	GPIO3_PP.01	121	Bidirectional	GPIO3_PG.01	369	Bidirectional
228	GPIO3_PN.01	393	Input	GPIO3_PE.06	38	Input	GPIO3_PU.05	485	Input
229	GPIO3_PO.00	400	Output	GPIO3_PP.00	120	Output	GPIO3_PG.00	368	Output
230	GPIO3_PH.01	345	Output	GPIO3_PE.07	39	Output	GPIO3_PV.06	494	Output
232	GPIO3_PI.03	355	Bidirectional	GPIO3_PF.00	40	Bidirectional	GPIO3_PW.00	496	Bidirectional
234	GPIO3_PI.04	356	Bidirectional	GPIO3_PF.01	41	Bidirectional	GPIO3_PW.01	497	Bidirectional
236	GPIO3_PCC.05	269	Output	GPIO3_PU.00	160	Output	GPIO3_PT.00	472	Output
238	GPIO3_PCC.06	270	Input	GPIO3_PU.01	161	Input	GPIO3_PT.01	473	Input
240	GPIO3_PEE.04	284	Input	GPIO3_PX.05	189	Input	GPIO3_PFF.00	528	Input

This Information is provided as is from Auvidea. Auvidea does not guarantee correctness but believes the numbers are correct. If you see any wrong information's, please let us know so we can correct the documentation.

11.3 How to calculate GPIOs

The above list should include every GPIO there is. This “how to” may help you find errors we did in our documentation ore to calculate GPIOs for upcoming models as the NVIDIA Jetson Orin.

11.3.1 GPIOnumber

The basic formular:

$$\mathrm{GPIOnumber} = \mathrm{GPIOletter}*8 + \mathrm{GPIOnumber} + \mathrm{GPIOoffset}$$

11.3.2 GPIOletter

The GPIOletter is located between [GPIO3_P] and [.*number*]

GPIOname	GPIOletter	GPIOletter (referenced)
GPIO3_PO.01	O	14 (for Xavier NX)
GPIO3_PCC.04	CC	2 (for Xavier NX)

This letter needs to be referenced to a number.
This number is individual to every Jetson module and can be found in the “tegra-gpio.h” (name may differ depending on module).
Pleas also see the example table shown in GPIOoffset

11.3.3 GPIOnumber

The GPIOnumber is easiest to get and can be extracted directly from the name.
GPIO3_PO.[GPIOnumber]

GPIOname	GPIOnumber
GPIO3_PO.01	1
GPIO3_PCC.04	4

11.3.4 GPIOoffset

The offset is connected to the GPIOletter. The same GPIOletter has always the same GPIOoffset for one specific module and only differs for AON cores.
GPIOoffsets are listed later in the table.

11.3.5 Example

Calculating GPIO number GPIO3_PO.01 for Jetson Xavier NX:

$$\mathrm{GPIOnumber} = \mathrm{GPIOletter}*8 + \mathrm{GPIOnumber} + \mathrm{GPIOoffset}$$

$$\mathrm{GPIOnumber} = 401 = 14*8 + 1 + 288$$

11.3.6 Table

Jetson Xavier NX			
Alpha Key	**Value**	**Offset**	**Note**
A.	0	288	
B.	1	288	
C.	2	288	
D.	3	288	
E.	4	288	
F.	5	288	
G.	6	288	
H.	7	288	
I.	8	288	
J.	9	288	
K.	10	288	
L.	11	288	
M.	12	288	
N.	13	288	
O.	14	288	
P.	15	288	
Q.	16	288	
R.	17	288	
S.	18	288	
T.	19	288	
U.	20	288	
V.	21	288	
W.	22	288	
X.	23	288	
Y.	24	288	
Z.	25	288	
AA	0	248	AON GPIO
BB	1	248	AON GPIO
CC	2	248	AON GPIO
DD	3	248	AON GPIO
EE	4	248	AON GPIO
FF	26	288	
GG	27	288	

Jetson Nano		
Alpha Key	**Value**	**Offset**
A.	0	0
B.	1	0
C.	2	0
D.	3	0
E.	4	0
F.	5	0
G.	6	0
H.	7	0
I.	8	0
J.	9	0
K.	10	0
L.	11	0
M.	12	0
N.	13	0
O.	14	0
P.	15	0
Q.	16	0
R.	17	0
S.	18	0
T.	19	0
U.	20	0
V.	21	0
W.	22	0
X.	23	0
Y.	24	0
Z.	25	0
AA	26	0
BB	27	0
CC	28	0
DD	29	0
EE	30	0
FF	31	0

Jetson TX2 NX			
Alpha Key	**Value**	**Offset**	**Note**
A.	0	320	
B.	1	320	
C.	2	320	
D.	3	320	
E.	4	320	
F.	5	320	
G.	6	320	
H.	7	320	
I.	8	320	
J.	9	320	
K.	10	320	
L.	11	320	
M.	12	320	
N.	13	320	
O.	14	320	
P.	15	320	
Q.	16	320	
R.	17	320	
S.	18	320	
T.	19	320	
U.	20	320	
V.	21	320	
W.	22	320	
X.	23	320	
Y.	24	320	
Z.	25	320	
AA	0	256	AON GPIO
BB	1	256	AON GPIO
CC	2	256	AON GPIO
DD	3	256	AON GPIO
EE	4	256	AON GPIO
FF	26	320	
GG	27	320	

SECTION 12 Appendix C [I2C]

12.1 I2C device bus

I2C Examples of configurations and how to use.

Bus	GEN1_I2C	GEN2_I2C	GEN3_I2C	CAM_I2C
Pins	185 and 187	189 and 191	232 and 234	213 and 215
Voltage (native)	3.3V	3.3V	1.8V	3.3V
Nano device				6
TX2 NX device	0			
Xavier NX device	1			2
Crypto chip		ATSHA204A		
CSI-2 camera	CSI-CD	CSI-E	CSI-F	CSI-AB
GPIO header	27 and 28	3 and 5		
EEPROM		24LC024		

12.2 I2C usage of devices and registers

12.2.1 List i2c devices on a specific bus

Syntax: i2cdetect [options] <busNr>

```
test@test-desktop:~$ i2cdetect -y -r 8
     0  1  2  3  4  5  6  7  8  9  a  b  c  d  e  f
00: -- -- -- -- -- -- -- -- -- -- -- -- --
10: -- -- -- -- -- -- -- -- -- -- -- -- -- -- -- --
20: -- -- -- -- -- -- -- -- -- -- -- -- -- -- -- --
30: -- -- -- -- -- -- -- -- -- -- -- -- -- -- -- --
40: -- -- -- -- -- -- -- -- -- -- -- -- -- -- -- --
50: -- -- -- -- -- -- -- -- -- -- -- -- -- -- -- --
60: -- -- -- -- -- -- -- -- -- -- -- -- -- -- -- --
70: -- -- -- -- -- -- 76 --
test@test-desktop:~$
```

12.2.2 Dump i2c device registers

Syntax: i2cdump [options] <busNr> <deviceAddress>

```
test@test-desktop:~$ i2cdump -y -f 8 0x76
No size specified (using byte-data access)
     0  1  2  3  4  5  6  7  8  9  a  b  c  d  e  f  0123456789abcdef
00: 00 00 ff ff 00 00 ff ff XX XX XX XX XX XX XX XX ........XXXXXXXX
10: XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XXXXXXXXXXXXXXXX
20: XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XXXXXXXXXXXXXXXX
...
d0: XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XXXXXXXXXXXXXXXX
e0: XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XXXXXXXXXXXXXXXX
f0: XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XXXXXXXXXXXXXXXX
test@test-desktop:~$
```

12.2.3 Set register value:

Syntax: i2cset [options] <busNr> <deviceAddress> <register> <address> <value>

```
test@test-desktop:~$ sudo i2cset -y -f 8 0x76 0x06 0x00
test@test-desktop:~$
```

12.2.4 Read register value:

Syntax: i2cget [options] <busNr> <deviceAddress> <register> <address>

```
test@test-desktop:~$ sudo i2cget -y -f 8 0x76 0x06
0x00
test@test-desktop:~$
```

12.2.5 Test IMX219 camera stream

The parameter `sensor-id=` describes the camera target. This id can be found by using `ls /dev/`. If the camera correctly plugged in then there should be a device called `/dev/videoX`, where X is the camera id.

```
test@test-desktop:~$ gst-launch-1.0 nvarguscamerasrc sensor-id=0 ! 'video/x-raw(memory:NVMM), width=(int)1280, height=(int)720, format=(string)NV12, framerate=(fraction)30/1' ! nvvidconv ! queue ! xvimagesink
```

SECTION 13 END OF DOCUMENT

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